

**TITLE OF THE INVENTION**

**LOW POWER CONSUMPTION COMPUTER PERIPHERAL DEVICE  
AND METHOD FOR REDUCING POWER CONSUMPTION**

**CLAIM OF PRIORITY**

[0001] This application makes reference to, incorporates the same herein, and claims all benefits accruing under 35 U.S.C §119 from an application entitled *Computer Peripheral Apparatus Consuming Low-Power And Method For Reducing Power Consumption Thereof* earlier filed in the Korean Industrial Property Office on 10 January 2001, and there duly assigned Serial No. 2001-1347 by that Office.

**BACKGROUND OF THE INVENTION**

**Field of the Invention**

[0002] The present invention relates to computer peripheral devices such as a printer, and more particularly, to a low power consumption computer peripheral device and a method for reducing power consumption therein.

**Description of the Related Art**

[0003] From the standpoint of power consumption, a conventional computer peripheral device such as a printer, does not operate interlocked with a computer, but independently operates and enters into a power save mode. Here, power save mode means a state in which a peripheral consumes less power than in a normal operating mode, and does not mean that the power supply to

1 the peripheral is completely cut off. For example, a conventional computer peripheral device having  
2 a power save mode can enter into a power save mode only when the peripheral is not performing its  
3 unique function for a predetermined time period, or the user requests a power save mode through  
4 manual operation from the outside.

5 [0004] In conclusion, even after the computer is turned off, the conventional computer peripheral  
6 device unnecessarily consumes power until a predetermined time period after its unique function has  
7 ended, or the user changes the operational state of the peripheral device.

## 8 SUMMARY OF THE INVENTION

9 [0005] To solve the above problems, it is an object of the present invention to provide a method  
10 for reducing power consumption of a computer peripheral device. The apparatus which can rapidly  
11 reduce power consumption by operating in conjunction with the computer from the standpoint of  
12 power consumption.

13 [0006] It is another object to provide a low power consumption computer peripheral device that  
14 uses the method for reducing power consumption.

15 [0007] To accomplish the above object of the present invention, there is provided a method for  
16 reducing power consumption in a computer peripheral device, in at least one or more computer  
17 peripheral devices connected to a computer so as to operate in conjunction with the computer, the  
18 method having the steps of (a) determining whether or not the peripheral has a first power save  
19 mode; (b) determining whether or not the computer is turned on, if it is determined that the  
20 peripheral has the first power save mode; (c) determining whether or not a predetermined time period  
21 has elapsed since the peripheral has ended its unique function, if it is determined that the computer  
22 is turned on; and (d) making the peripheral device enter into the first power save mode, if it is

1 determined that the predetermined time period has elapsed, or that the computer is not turned on, in  
2 which the first power save mode corresponds to a state in which the peripheral consumes less power  
3 than when the peripheral is performing its unique function.

4 **[0008]** To accomplish another object of the present invention, there is also provided a computer  
5 peripheral device consuming less power in a system having a computer, at least one or more  
6 peripheral devices operating in conjunction with the computer, and communication cables  
7 connecting the computer with the peripheral devices, the computer peripheral apparatus having a  
8 power unit having a primary part for converting alternating current (AC) power input from the  
9 outside into direct current (DC) power, and a secondary part for providing the DC power as the  
10 power for the peripherals, in response to a power control signal; a counter for performing a counting  
11 operation in response to a counting-start signal, and outputting the counted result; a comparison unit  
12 for comparing the counted result with a predetermined time period, and outputting the compared  
13 result; and a control unit for outputting the power control signal in response to the compared result  
14 or a power identification signal, which has a predetermined level when the computer is turned on and  
15 is input from the communication cable, checking whether or not the unique function of the peripheral  
16 is performed, and outputting the counting-start signal in response to the checked result, in which the  
17 power identification signal has a level varying in response to the on/off power state of the computer.

#### 18 **BRIEF DESCRIPTION OF THE DRAWINGS**

19 **[0009]** A more complete appreciation of the present invention, and many of the attendant  
20 advantages thereof, will become readily apparent as the same becomes better understood by  
21 reference to the following detailed description when considered in conjunction with the  
22 accompanying drawings, wherein:

1 [0010] FIGs. 1A and 1B are a flowchart for explaining a preferred embodiment of a method for  
2 reducing power consumption of a computer peripheral device consuming less power according to  
3 the present invention; and

4 [0011] FIG. 2 is a block diagram of a computer peripheral apparatus consuming less power,  
5 according to the present invention, which performs the method for reducing power consumption  
6 shown in FIG. 1.

### 7 DETAILED DESCRIPTION OF THE INVENTION

8 [0012] Hereinafter, embodiments of the present invention will be described in detail with  
9 reference to the attached drawings. The present invention is not restricted to the following  
10 embodiments, and many variations are possible within the spirit and scope of the present invention.  
11 The embodiments of the present invention are provided in order to more completely explain the  
12 present invention to anyone skilled in the art.

13 [0013] FIGs. 1A and 1B are a flowchart for explaining a preferred embodiment of a method for  
14 reducing power consumption of a computer peripheral apparatus according to the present invention.  
15 The method includes steps 20 through 30 for making a peripheral, which has a first power save  
16 mode, enter into the first power save mode, and steps 32 through 42 for making the peripheral exit  
17 from the first power save mode in response to whether or not the cause, by which the peripheral  
18 entered into the first power save mode, is removed.

19 [0014] The computer peripheral enters into a first power save mode in step 20 through 30. Here,  
20 the first power save mode is a state in which the computer peripheral device consumes less power  
21 than when the peripheral device performs its unique function. Therefore, in the first power save  
22 mode, power to the computer peripheral device is reduced, but not completely cut off. To do this,

1 first, the computer is connected to at least one or more computer peripheral devices, each of which  
2 operates in conjunction with the computer in step 20. At this time the peripheral device is enabled  
3 to receive a command to enable it to perform its unique function.

4 **[0015]** After step 20, it is determined whether or not each computer peripheral has the first power  
5 save mode in step 22. If it is determined that a computer peripheral does not have the first power  
6 save mode, the method for reducing power consumption according to the present invention shown  
7 in FIGs. 1A and 1B is finished. However, if it is determined that a computer peripheral has the first  
8 power save mode, the computer peripheral determines whether or not the computer is turned on in  
9 step 24. Here, if power is not supplied to the computer, or the connection between the computer and  
10 the peripheral is cut off, it is determined that the computer is not turned on. Also, if the computer  
11 is in a power save mode, in which the computer consumes less power than when the computer is in  
12 a normal operating mode, it is determined that the computer is not turned on.

13 **[0016]** If it is determined that the computer is turned on, it is then determined, in step 26, whether  
14 or not a predetermined time has elapsed since the computer peripheral has last performed its unique  
15 function. For example, if the computer peripheral is a printer, it is determined whether or not a  
16 predetermined time period has elapsed after the last print operation was performed.

17 **[0017]** If it is determined that the predetermined time period, after the computer peripheral device  
18 has ended its unique function, has not elapsed, it is determined in step 28 whether or not a user  
19 requested that the computer peripheral enter into the first power save mode. Here, if it is determined  
20 that the user has not requested the computer peripheral device to enter into the first power save  
21 mode, the process returns to step 26, and steps 26 and 28 are is repeatedly performed until either the  
22 predetermined time period elapses or until the user requests the that the computer peripheral enter  
23 into the first power save mode. It should be understood here that if the peripheral device performs

1 its unique function before the predetermined time period elapses, the predetermined time period is  
2 restarted.

3 **[0018]** If it is determined in step 24 that the computer is not turned on, or if it is determined in step  
4 26 that the predetermined time period has elapsed, or if it is determined in step 28 that user has  
5 requested the computer peripheral device to enter into the first power save mode, step 30 is  
6 performed and the computer peripheral is made to enter into the first power save mode.

7 **[0019]** Alternatively, step 28 can be selectively excluded. For example, the method for reducing  
8 power consumption according to the present invention can exclude step 28, and in this case, it is  
9 determined in step 26 whether or not a predetermined time period has elapsed after the computer  
10 peripheral has ended its unique function. If it is determined that the predetermined time period has  
11 elapsed, step 30 is performed. Also, step 28 can be performed between steps 22 and 24, or between  
12 steps 24 and 26. Regardless of where step 28 is in the process, step 30 is performed when the user  
13 requests the computer peripheral to enter into the first power save mode.

14 **[0020]** As discussed above, if it is determined that the user requests that the computer peripheral  
15 enter into the first power save mode, if a predetermined time period has elapsed after the computer  
16 peripheral last performed its unique function or if it is determined that the computer is not turned on,  
17 the computer peripheral is made to enter into the first power save mode in step 30.

18 **[0021]** Meanwhile, after step 30, in order to leave the first power save mode, the process checks,  
19 in steps 32-42, for the condition that caused the peripheral device to enter into the first power save  
20 mode, and whether or not that condition has been removed.

21 **[0022]** Accordingly, in step 32 the process checks to determine whether the first power save mode  
22 was the result of step 24 determining that the computer was not turned on. If so, then step 34  
23 repeatedly checks whether the computer has been turned on, and once the computer has been turned

1 on the peripheral device exits from the first power save mode in step 42.

2 **[0023]** If it is determined in step 32 that the cause for entering into the first power save mode was  
3 not due to the computer not being turned on, then in step 36 it is checked to determine whether the  
4 first power save mode was the result of step 26 determining that the predetermined time period had  
5 elapsed since the peripheral device last performed its unique function. If so, then step 38 repeatedly  
6 checks for a command signal from the computer requesting the peripheral device perform its unique  
7 function. When the command signal is detected by the peripheral device, the peripheral device exits  
8 from the first power save mode in step 42.

9 **[0024]** If it is determined in step 36 that the cause for entering into the first power save mode was  
10 not due to the predetermined time period having elapsed since the peripheral device last performed  
11 its unique function, then it is determined that the peripheral device entered into the first power save  
12 mode as a result of the user request in step 28. Accordingly, step 40 repeatedly checks for a user's  
13 request for the peripheral device to exit the first power save mode, and upon such a request the  
14 peripheral device exits from the first power save mode in step 42.

15 **[0025]** The structure and operation of a computer peripheral apparatus consuming less power,  
16 according to the present invention, which performs the method for reducing power consumption, will  
17 now be explained.

18 **[0026]** FIG. 2 is a block diagram of a computer peripheral device consuming less power,  
19 according to the present invention, which performs the method for reducing power consumption  
20 shown in FIGs. 1A and 1B. The computer peripheral device shown in FIG. 2 includes power unit  
21 70, counter 72, comparison unit 74, and control unit 76.

22 **[0027]** Though one computer 60 and one computer peripheral device 62 are connected to each  
23 other in FIG. 2, a plurality of computer peripheral devices 62 according to the present invention can

1 be connected to one computer 60. If a plurality of similar or uniquely different types of computer  
2 peripheral devices 62 are connected to one computer, each computer peripheral device has the same  
3 illustrated components as that of the computer peripheral device 62, according to the present  
4 invention, and each computer peripheral device controlled to perform its unique function by being  
5 linked to the computer via a communication cable 80.

6 **[0028]** The power unit 70 of the computer peripheral device 62 has a primary part (not shown) and  
7 a secondary part (not shown). Here, the primary part converts alternating current (AC) power, which  
8 is input through the input terminal IN1 from the outside, into direct current (DC) power, and outputs  
9 the DC power to the secondary part. In response to a power control signal output from the control  
10 unit 76, the secondary part outputs the DC power as power which each unit 72 through 76 of the  
11 computer peripheral device 62 needs.

12 **[0029]** To perform steps 24 and 30, the control unit 76 outputs a power control signal to the power  
13 unit 70 in response to a power identification signal 82, which has a predetermined level when the  
14 computer 60 is turned on and is input via the communication cable 80. For example, if the level of  
15 the power identification signal 82 is not at a predetermined level, the control unit 76 recognizes that  
16 the computer is not turned on, and outputs a power control signal, which controls the secondary part  
17 of the power unit 70 not to generate power needed in each unit 72 through 76 of the computer  
18 peripheral device 62, to the power unit 70. Here, if the power unit 70 does not supply power to units 72  
19 through 76 in response to the power control signal, the computer peripheral device 62 enters into the  
20 first power save mode.

21 **[0030]** At this time, since the computer 60 is determined to be turned off (step 24) due to the  
22 computer 60 being in its own power save mode (hereafter: second power save mode) or the  
23 connection between the computer 60 and the computer peripheral device 62 is cut off, and the level of the



1 power identification signal 82 is not the same as the predetermined level, the control unit 76  
2 identifies (step 32) the cause for entering into the first power save mode as being due to the computer  
3 60 being turned off. Accordingly, control unit 76 continuously checks (step 34) for when the power  
4 identification signal 82 returns to the predetermined level, which will indicate that computer 60 is  
5 turned on. When it is determined that the computer 60 is turned on, control unit 76 controls the  
6 computer peripheral 62 to exit the first power save mode by outputting the power control signal,  
7 which controls the secondary part of the power unit 70 to provide power again to each unit 72  
8 through 76 of the computer peripheral 62, to the power unit 70.

9 **[0031]** Meanwhile, the power identification signal 82 is a signal having a level which varies in  
10 response to whether the computer 60 is turned on or off. The power identification signal 82 will now  
11 be explained in detail.

12 **[0032]** A signal transmitted through one line (hereinafter, referred to as "an identification line")  
13 among a plurality of lines of the communication cable 80, through which data needed for performing  
14 the unique function of the computer peripheral 62 is transmitted from the computer 60 to the  
15 computer peripheral 62, is determined as the power identification signal 82. At this time, a line,  
16 which transmits signal having the greatest level change when the computer 60 is turned off, among  
17 the plurality of lines is determined as the identification line. The identification line is selected  
18 according to the type of communication cable 80 as explained below.

19 **[0033]** According to an embodiment of the present invention, the communication cable 80 can be  
20 individual cable which connects the computer 60 to each of the computer peripherals. For example,  
21 if a "1284 cable" having a strobe line, a busy line, and 8-bit data lines, is used as an individual cable,  
22 the power identification signal 82 is transmitted through an arbitrary data line selected as the  
23 identification line among the 8-bit data lines. This is because when the computer 60 is turned off,

1 or in a second power save mode, a signal having a logic "low" level is not transmitted from the  
2 computer 60 to the computer peripheral 62 through data lines, and therefore, the computer peripheral  
3 62 recognizes the power identification signal 82 as a logic "high" level. That is, if the power  
4 identification signal 82 is recognized as a logic "high" level, the control unit 76 of the computer  
5 peripheral 62 determines that the computer 60 is not turned on. However, if the computer 60 is  
6 turned on, a signal having a logic "low" level is transmitted from the computer 60 to the peripheral  
7 62 through data lines. Therefore, if the power identification signal 82 is recognized as a logic "low"  
8 level, the control unit 76 can determine that the computer 60 is turned on.

9 **[0034]** According to another embodiment of the present invention, the communication cable 80  
10 can be a common cable commonly connecting the computer 60 to the computer peripherals. For  
11 example, if a universal serial bus (USB) having a voltage bus (VBUS) and a ground line is used as  
12 the common cable, the power identification signal 82 can be input through the voltage bus  
13 corresponding to the identification line. This is because if the computer 60 is turned off, or the  
14 computer 60 is in the second power save mode, the power identification signal having a logic "high"  
15 level is not transmitted from the computer 60 to the peripheral 62 through the voltage bus, and  
16 therefore, the computer peripheral 62 recognizes that the power identification signal 82 is a logic  
17 "low" level. That is, if a power identification signal 82 having a logic "low" level is input, the  
18 control unit 76 of the computer peripheral 62 determines that the computer 60 is not turned on.  
19 However, if the computer 60 is turned on, a power identification signal 82 having a "high" logic level  
20 is transmitted from the computer 60 to the peripheral 62 through the voltage bus. Therefore, if the  
21 power identification signal having a logic "high" level is input through the voltage bus, the control  
22 unit 76 determines that the computer 60 is turned on.

23 **[0035]** As described above, when the computer 60 is turned on, the predetermined level of the

1 power identification signal 82 becomes a logic "low" or logic "high" level according to the type of  
2 the communication cable.

3 **[0036]** Also, the control unit 76 checks whether or not the computer peripheral 62 performs its  
4 unique function, and in response to the result of checking, outputs a counting-start signal to the  
5 counter 72. At this time, in response to the counting-start signal provided from the control unit 76,  
6 the counter 72 performs a counting operation, and outputs the counting result to the comparison unit  
7 74. The comparison unit 74 compares the counting result with the predetermined time period, and  
8 outputs the comparison result to the control unit 76. For example, if the level of the power  
9 identification signal 82 is the predetermined level, the control unit 76 determines (step 24) that the  
10 computer 60 is turned on, and checks the comparison result output by the comparison unit 74 to  
11 perform step 26.

12 **[0037]** That is, after the computer peripheral 62 performs its unique function a first time, the  
13 control unit 76 outputs the counting-start signal to the counter 72 so that the counter 72 starts a  
14 counting operation, and by checking the result compared in the comparison unit 74, recognizes  
15 whether or not the result counted in the counter 72 corresponds to the predetermined time period.

16 **[0038]** When it is determined that the result counted in the counter 72 is greater than the counting  
17 value corresponding to the predetermined time period, the control unit 76 outputs the power control  
18 signal which controls the secondary part of the power unit 70 not to generate power, to the power  
19 unit 70, thus the computer peripheral 62 enters into the first power save mode at step 30.

20 **[0039]** Accordingly, power control unit 76 determines (step 36) that the cause for entering into  
21 the first power save mode was the elapse of the predetermined time period since the computer  
22 peripheral last performed its unique function. At this time, control unit 76 continuously checks (step  
23 38) for a request from computer 60 for the computer peripheral 62 to again performs its unique

1 function. When such a request is detected, control unit 76 outputs, to power unit 70, the power  
2 control signal which controls the secondary part of power unit 70 to generate power to units 72-76.

3 **[0040]** Meanwhile, to perform steps 28 and 30, the control unit 76 receives a power save request  
4 signal, which requests the computer peripheral 62 to enter into the first power save mode, from the  
5 external user through the input terminal IN2, and in response to the received power save request  
6 signal, outputs, to the power unit 70, the power control signal which controls the secondary part of  
7 the power unit 70 to not provide power to each unit 72 through 76 of the computer peripheral 62.

8 **[0041]** Then, since power control unit 76 caused the computer peripheral 62 to enter into the first  
9 power save mode at the request of the user, was control unit 76 continuously checks for user input,  
10 via input terminal IN2, of a power save exit request signal (step 40), which requests the computer  
11 peripheral 62 to exit from the first power save mode. When the power save exit request signal is  
12 detected, control unit 76 controls the secondary part of power unit 70 to provide power to units 72-  
13 76.

14 **[0042]** As described above, unlike a conventional computer peripheral, which constantly  
15 consumes power unnecessarily when the computer is turned off because the computer peripheral  
16 operates independently from the computer from the standpoint of power consumption, the computer  
17 peripheral apparatus, consuming less power and the method for reducing power consumption thereof  
18 according to the present invention, recognizes the power state of the computer 60 through the power  
19 identification signal 82 by the control unit 76, and can actively enter into or exit from the first power  
20 save mode, and therefore can reduce unnecessary power consumption in at least one or more  
21 peripherals 62 which are used dependently of the computer 60.